



This image shows a detailed microchip layout of a detector readout integrated circuit. The layout is rectangular and filled with various functional blocks and interconnects. A central green area represents the 16 analogue channels. To the right is a large blue and yellow block for the digital port. At the bottom are the TDC ramp, ADC ramp, and Dual DAC blocks. The top left contains the bias and supply circuitry, and the bottom left features the bandgap reference. The entire chip is surrounded by a thick orange border, likely representing the bonding pads.

Bias + supply

16 analogue
channels

Digital port

TDC ramp

ADC ramp

Bandgap

Dual DAC